

IN THE SPECIFICATION

On page 15 of the instant specification, in line 16, please change --130-- to [140]. The frame buffer is element 140, as shown in Figure 2 of the present application.

IN THE DRAWINGS

Applicants respectfully propose to amend Figure 1 to show that the frame buffer is element 140, as shown in Figure 2 of the present application. A submission of the proposed drawing amendment containing a red-lined Figure 1 is being separately submitted concurrently with this preliminary amendment.

IN THE CLAIMS

Please enter the following amendments before examination:

1. (Amended) A computer system, comprising:
 - a processor for performing geometric calculations on a plurality of vertices of a primitive;
 - a rasterization circuit coupled to the processor that rasterizes the primitive according to a rasterization process which operates [on] using a floating point format;
 - a frame buffer coupled to the rasterization circuit for storing a plurality of [color] image values in the floating point format; and
 - a display screen coupled to the frame buffer for displaying an image according to the [color] image values stored in the frame buffer.